Standard Specifications for

Ferrite Pot Style Cores

The International Magnetics Association

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FOREWORD

This standard on ferrite pot cores was developed by the engineering committee of the Soft Ferrite Division of the Magnetic Materials Producers Association. Several International Electrotechnical Commission documented recommendations have been included in this standard. The specific IEC publications that have been used in total or in part are:

- **IEC 133** Dimensions for pot cores of ferromagnetic oxides and associated parts.
- **IEC 133A** First supplement to Publication 133.
- **IEC 133B** Second supplement to Publication 133, Amendment #1
- **IEC 205** Calculation of effective parameters of magnetic piece parts.

ISO Recommendations R370 was used in the conversion of tolerated dimensions from inches into millimeters and vice versa.

This pot core standard is only an advisory document and its use or adaptation is entirely voluntary.

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1.0 SCOPE

This standard defines a series of ferrite pot cores, and their dimensions and tolerances. This range of ferrite pot cores is in accordance with the international standard, IEC Publication 133, “Dimensions for pot cores made of ferromagnetic oxides,” and supplements IEC 133A and 133B.

To further enhance the use of standard ferrite pot cores, bobbin design information and a visual inspection specification are included in this standard.

2.0 POT CORE DIMENSIONS AND TOLERANCES

The physical dimensions of the standard series of pot cores shall be in accordance with Table 1. For dimensional details see Figure 1.

![Figure 1 Pot Cores](image-url)
3.0 BOBBIN DIMENSIONS

3.1 After the bobbin has been wound, the wound bobbin shall be within the outline dimensions shown in Table 2 and Figure 2.

<table>
<thead>
<tr>
<th>SIZE</th>
<th>TOL.</th>
<th>d₂</th>
<th>d₃</th>
<th>h₂</th>
<th>R₁</th>
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<tr>
<td></td>
<td>mm</td>
<td>in</td>
<td>mm</td>
<td>in</td>
<td>mm</td>
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<tr>
<td>9x5</td>
<td>MIN.</td>
<td>—</td>
<td>—</td>
<td>4</td>
<td>0.157</td>
</tr>
<tr>
<td></td>
<td>MAX.</td>
<td>7.4</td>
<td>0.291</td>
<td>3.6</td>
<td>0.142</td>
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<tr>
<td>11x7</td>
<td>MIN.</td>
<td>—</td>
<td>—</td>
<td>4.8</td>
<td>0.189</td>
</tr>
<tr>
<td></td>
<td>MAX.</td>
<td>8.9</td>
<td>0.35</td>
<td>4.4</td>
<td>0.173</td>
</tr>
<tr>
<td>14x8</td>
<td>MIN.</td>
<td>—</td>
<td>—</td>
<td>6.1</td>
<td>0.24</td>
</tr>
<tr>
<td></td>
<td>MAX.</td>
<td>11.5</td>
<td>0.453</td>
<td>5.6</td>
<td>0.22</td>
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<tr>
<td>18x11</td>
<td>MIN.</td>
<td>—</td>
<td>—</td>
<td>7.7</td>
<td>0.303</td>
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<tr>
<td></td>
<td>MAX.</td>
<td>14.8</td>
<td>0.583</td>
<td>7.2</td>
<td>0.283</td>
</tr>
<tr>
<td>22x13</td>
<td>MIN.</td>
<td>—</td>
<td>—</td>
<td>9.5</td>
<td>0.374</td>
</tr>
<tr>
<td></td>
<td>MAX.</td>
<td>17.8</td>
<td>0.701</td>
<td>9.2</td>
<td>0.362</td>
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<tr>
<td>26x16</td>
<td>MIN.</td>
<td>—</td>
<td>—</td>
<td>11.6</td>
<td>0.457</td>
</tr>
<tr>
<td></td>
<td>MAX.</td>
<td>21.1</td>
<td>0.831</td>
<td>11</td>
<td>0.433</td>
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<tr>
<td>30x19</td>
<td>MIN.</td>
<td>—</td>
<td>—</td>
<td>13.6</td>
<td>0.535</td>
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<tr>
<td></td>
<td>MAX.</td>
<td>24.9</td>
<td>0.98</td>
<td>13</td>
<td>0.512</td>
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<tr>
<td>36x22</td>
<td>MIN.</td>
<td>—</td>
<td>—</td>
<td>16.3</td>
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<tr>
<td></td>
<td>MAX.</td>
<td>29.8</td>
<td>1.173</td>
<td>14.6</td>
<td>0.575</td>
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<tr>
<td>42x29</td>
<td>MIN.</td>
<td>—</td>
<td>—</td>
<td>17.8</td>
<td>0.701</td>
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<tr>
<td></td>
<td>MAX.</td>
<td>35.5</td>
<td>1.398</td>
<td>20.3</td>
<td>0.799</td>
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3.2 The dimensions of the bobbins that will fit the standard range of pot cores are shown in Figure 3 and Table 3. Printed circuit version of some of these bobbins are listed in Table 4 and detailed in Figures 4 and 5.
<table>
<thead>
<tr>
<th>SIZE (mm)</th>
<th>TOL</th>
<th>P</th>
<th>S&lt;sub&gt;1&lt;/sub&gt;</th>
<th>S&lt;sub&gt;2&lt;/sub&gt;</th>
<th>FIG.</th>
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<tbody>
<tr>
<td></td>
<td></td>
<td>mm</td>
<td>in</td>
<td>mm</td>
<td>in</td>
</tr>
<tr>
<td>14x8</td>
<td>MIN.</td>
<td>4.62</td>
<td>.182</td>
<td>3.45</td>
<td>.136</td>
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<tr>
<td></td>
<td>MAX.</td>
<td>4.88</td>
<td>.192</td>
<td>3.66</td>
<td>.144</td>
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<tr>
<td>18x11</td>
<td>MIN.</td>
<td>4.57</td>
<td>.180</td>
<td>3.43</td>
<td>.135</td>
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<tr>
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<td>MAX.</td>
<td>4.95</td>
<td>.195</td>
<td>3.68</td>
<td>.145</td>
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<tr>
<td></td>
<td>MIN.</td>
<td>MAX.</td>
<td>4.44</td>
<td>.175</td>
<td>3.43</td>
</tr>
<tr>
<td>---------</td>
<td>------</td>
<td>------</td>
<td>------</td>
<td>------</td>
<td>------</td>
</tr>
<tr>
<td>22x13</td>
<td></td>
<td></td>
<td>4.95</td>
<td>.195</td>
<td>3.68</td>
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<tr>
<td>26x16</td>
<td></td>
<td></td>
<td>4.62</td>
<td>.182</td>
<td>3.45</td>
</tr>
</tbody>
</table>

For minimum winding areas for the different bobbin sizes and design configurations see Table 3.

**4.0 CALCULATION OF DIMENSIONAL PARAMETERS OF POT CORES**

The method used here is recommended for the calculation of the dimensional parameters of pot cores and is in accordance with IEC Publication 205, “Calculation of Effective Parameters of Magnetic Piece Parts.”

4.1 For this method of calculating the dimensional parameters of pot cores, the pot core set is substituted by an ideal toroidal core such that a coil wound on that toroid would give exactly the same electrical performance as a coil with same number of turns placed on the pot core set.

4.2 The dimensional parameters of that substitute toroid are called effective parameters. These are indicated by the suffix “e” added to the symbol.

Magnetic path length \( l_e \text{ mm} \)
Cross-sectional area \( A_e \text{ mm}^2 \)
Core volume \( V_e \text{ mm}^3 \)

4.3 For the purpose of the calculation of the dimensional parameters, the closed magnetic circuit of a pot core set is divided into five sections. For each section the area, flux path length and the core constants \( C_1 \) and \( C_2 \) are determined. See Figure 6.

\[
C_1 = \frac{\ell}{A} \text{ mm}^{-1} \quad \text{and} \quad C_2 = \frac{\ell}{A^2} \text{ mm}^{-1} \text{ mm}^{-3}
\]

The core constants for the total magnetic circuit of the pot core set are:

\[
C_1 = \Sigma \frac{\ell}{A} \text{ mm}^{-1} \quad \text{and} \quad C_2 = \Sigma \frac{\ell}{A^2} \text{ mm}^{-1} \text{ mm}^{-3}
\]

From these core constants the effective dimensional pot core parameters can be calculated.

- Magnetic path length \( \ell_e = \frac{C_1^2}{C_2} \text{ mm} \)
- Cross-sectional area \( A_e = \frac{C_1}{C_2} \text{ mm}^2 \)
- Core volume \( V_e = \ell_e A_e = \frac{C_1^3}{C_2^2} \text{ mm}^3 \)

4.4 For each of the five sections of the magnetic circuit of a pot core set, the magnetic path length and cross-sectional area has to be determined:

Area of centerpost, \( A_3 + A_3' + A_3'' \)

The condition to obtain \( A_3' \) \( A_3'' \) is:

\[
S_1 = r_2 - \sqrt{\frac{r_2^2 + r_1^2}{2}}
\]

Area of outer ring.

The condition to obtain \( A_1' \) \( A_1'' \) is:

\[
S_2 = \sqrt{r_2^2 + r_1^2}
\]
Cross sectional area of centerpost. \[ A_1 = \pi \left( r_2 - r_1 \right) \left( r_2 + r_1 \right) \text{mm}^2 \]

Cross sectional area for outer ring, \[ A_1 = \pi \left( r_4 - r_3 \right) \left( r_4 + r_3 \right) \text{mm}^2 \]

\( \ell / A \) for two plates. \[ \ell_2/A_2 = \frac{1}{\pi h} \ln \frac{r_3}{r_2} = \frac{.733}{h} \log \frac{r_3}{r_2} \text{mm}^{-1} \ell_2/A_2 \]

for two plates, \[ \ell_2/A_2^2 = \frac{1}{2\pi^2 h^2} x r_3 - r_2 \frac{r_3 r_2}{r_3 + r_2} = .733/\text{mm}^3 \]

Mean flux path length at corners. \[ \ell_4 = \ell'_4 + \ell''_4 = \frac{\pi}{4} (h + 2S_2) \text{mm} \]

Cross sectional area associated with \( \ell_4 \) \[ A_4 = \frac{\pi}{4} \left( r_4^2 - r_3^2 + 2 r_3 h \right) \]

Mean flux path length at corners, \[ \ell_5 = \ell'_5 + \ell''_5 = \frac{\pi}{4} \left( h + 2S_1 \right) \text{mm} \]

Cross sectional area associated with \( \ell_5 \), \[ A_4 = \frac{\pi}{2} \left( r_2^2 - r_1^2 + 2 r_2 h \right) \text{mm}^2 \]

4.5 The above calculations ignore the effects of wire slots corrections:

From \( A_1 \) subtract: \[ ng \left( r_4 - r_3 \right) \]

Multiply \( \ell_2/A_2 \) by: \[ \left[ \frac{1}{1 - \frac{ng}{2\pi} r_3} \right] \]

Multiply \( \ell_2/A_2^2 \) by: \[ \left[ \frac{1}{\left( 1 - \frac{ng}{2\pi} r_3 \right)^2} \right] \]

Multiply \( A_4 \) by: \[ \left[ 1 - \frac{ng}{\pi} \left( r_4 - r_3 \right) \right] \]

\[ n = \text{number of wire slots} \]
\[ g = \text{slot width} \]

5.0 SURFACE CONDITIONS AND APPEARANCE OF POT CORES

5.1 Cleanliness
All surfaces of the pot core should be free of dirt or any other foreign matter. (Any stains, discolorations or surface crazings are allowed if they do not interfere with the performance of the pot core.)

5.2 Visual Appearance of Pot Cores

5.2.1 Mating Surfaces (See Figure 7)
For the purpose of this visual specification the mating surfaces do not include the areas adjacent to the wire slots nor the centerpost if it is gapped. These areas will be considered separately in 5.2.2 and 5.2.4.
5.2.1.1 Chips (See Figure 7)
Chips penetrating from the edges onto the mating surface area shall not exceed 50% of the width of the surface containing the defect. The total number of chips acceptable for the size stated above and located on the outer wall (OW) surfaces shall not exceed:
4. Core size 1811 and smaller, and
6. Core size 2213 and larger.

The total number of chips acceptable for the size stated above on the centerpost surface (IW) shall not exceed 2 for all pot core sizes.
A ragged edge or a series of small chips, less than 25% of the width, clustered or strung out together on edges is allowable for 20% of the circumference of the centerpost. One series of chips on each half outer wall and one series of chips on the centerpost is allowed.

5.2.1.2 Cracks (See Figure 7)
Cracks on the outer wall surface that are less than 50% of the width of the outer wall are allowed. Sum total of crack lengths is not to exceed the width of the outer wall.
Cracks on the centerpost surface that are less than 50% of the width of the inner wall are allowed. Sum total of crack lengths is not to exceed the width of the inner wall.

5.2.2 Wire Slot and Wire Way Areas (See Figures 8, 9 and II)

5.2.2.1 Flashings (See Figure 8)
There shall be no flashings extending from the core into the wire slot.
5.2.2.2 Chips (See Figures 8 and 9)

From the mating surface side (Fig. 8), chips 100% across width of outer wall and same in length along the mating surface are acceptable.

From the back surface (Fig. 9), chips 100% across width of the outer wall and same in length along the back surface are acceptable.

Chips from the mating surface down the edge of the wire slot and from the back surface down the edge of the wire slot with a length of less than 50% of the core height are acceptable. (Fig. 8)
5.2.2.3 Cracks (See Figures 8 and 11)

Cracks at the edge of the wire way (Fig. 8) going into the back wall, with a length less than half the distance from the edge of the wire way to the centerpost, are acceptable.

Cracks at the wire slot (Fig. 11) less in length than the width of the outer wall are allowed. Total number of cracks acceptable per above shall not exceed 4 per core.

5.2.3 Back Surface (See Figure 9)

5.2.3.1 Chips (See Figure 9)

The maximum dimension of a chip on the back surface of a core should be less than 50% of the height of the core. The total number of chips, acceptable to the above, shall not exceed 4.

A ragged edge or a series of small chips, less than 25% of the width of the outer wall, clustered or strung out together on the edge is allowable for 20% of the circumference of the pot core outer wall.
5.2.3.2 Cracks (See Figure 9)

Surface cracks parallel to the wire way are permissible.

Surface radial cracks from the center hole, up to 10% of the core diameter in length, are allowed.

5.2.4 Centerpost (See Figure 10)

The following applies for those pot cores that have a recessed centerpost

5.2.4.1 Chips (See Figure 10)

A series of small chips, less than 25% of the width of the inner wall, resulting in a ragged edge around the centerpost edge, is permissible. No chip on the surface shall be greater than 50% of the width of the wall.

Chips on the outer diameter of the centerpost with a maximum dimension of less than 25% of the height of the centerpost are permissible. The total number of chips acceptable for the size stated above is 3.

5.2.5 Coil Space (See Figure 10)

5.2.5.1 Cracks (See Figure 10)

Cracks at the base of the centerpost and the base of the outer wall are permissible.

5.2.5.2 Pull-outs (See Figure 10)

Pull-outs in the bottom surface of the pot core that are less than 25% of the bottom area are acceptable.

5.2.6 Exterior Outer Wall Surfaces (See Figure 11)

The areas adjacent to the wire slot are not included and are covered in 5.2.2.

5.2.6.1 Chips (See Figure 11)

Chips with a maximum dimension of less than 50% of the core height are allowed. The total number of chips acceptable to the above shall not exceed 5.

5.2.6.2 Cracks (See Figure 11)

Vertical cracks that are less than 25% of the core height in length are acceptable. The total number of vertical cracks acceptable to the above shall not exceed 2.

Horizontal cracks that are less than twice the outer wall thickness in length are acceptable. The total number of horizontal cracks acceptable to the above shall not exceed 2.